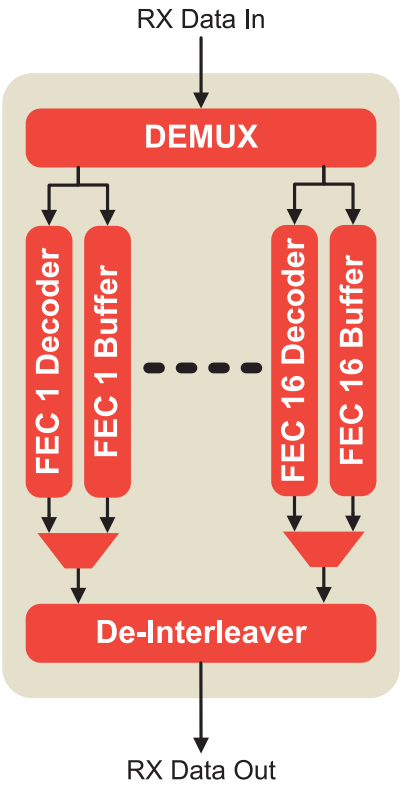
G.709 GFEC RX IP Core

Applications

- Optical transport networks
- Wide area networks

Hardent ITU-T G.709 GFEC RX IP



Hardent's IP portfolio offers customers ready-made solutions to accelerate product development and meet demanding time-to-market schedules.

Developed by a team of experienced FPGA and ASIC designers, Hardent's IP cores have undergone extensive verification and offer proven interoperability and compatibility.

Description

The ITU-TG.709 GFEC Receiver IP Core implements Reed-Solomon FEC (Forward Error Correction) as specified by the ITU-T G.709 Annex A specification.

The GFEC de-interleaves the data to the OTU sub-rows.

Key Features

- ITU-T G.709 Annex A compliant
- Reed-Solomon RS(255,239) FEC, 8-bit symbols
- De-interleaves data from the 16 FEC decoders to the 16 OTU sub-rows
- Supports OTU-1/2/3 rates
- Performance monitoring and statistics counters

Deliverables

- Encrypted RTL source code IP core
- Functional and structural coverage reports
- Comprehensive integration guide
- Technical support and maintenance updates

Product Options

- IP customization and integration services available on request
- Multi-project licenses available
- UVM verification bindable modules

G.709-FEC-RX _prodbrief-v1.0

Hardent
ENGINEERING | CONSULTING | TRAINING

For additional information contact:
info@hardent.com
+1-514-284-5252
www.hardent.com

Copyright © 2018 Hardent Inc.